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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets necessary)				COMPLETE IF KNOWN	
				Application Number	09/845685
				Filing Date	April 30, 2001
				First Named Inventor	Wenhua Lin
				Group Art Unit	2882
Examiner Name	Krystyna Suchecki				
SHEET	1	OF	1	Docket Number	LIGHT1920

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
	1	5,136,671		Dragone	Aug. 4, 1992	
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	4	5,751,872		Bissessur et al.	May 12, 1998	
	5	US 6,175,671 B1		Roberts	Jan. 16, 2001	
	6	US 6,222,957 B1		Lin et al.	Apr. 24, 2001	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (If known)				
	11	PCT	WO 98/43128		The Secretary of State for Defence [GB/GB]	10-1-1998		
	12	PCT	WO 99/45420		The University of Melbourne [AU/AU]	9-10-1999		
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶	
	15	AMERSFOORT, et al., <i>Passband Broadening of Integrated Arrayed Waveguide Filters Using Multimode Interference Couplers</i> , Electronics Letters, 29 th February 1996, Vol. 32, No. 5, pp.449-451.		
	16	SUGITA, et al., <i>Very Low Insertion Loss Arrayed-Waveguide Grating with Vertically Tapered Waveguides</i> , IEEE Photonics Technology Letters, Vol. 12, No. 9, September 2000, pp. 1180-1182.		
	17	KASAHARA, et al., <i>Low-Power Consumption Silica-Based 2 x 2 Thermo-optic Switch Using Trenched Silicon Substrate</i> , IEEE Photonics Technology Letters, Vol 11, No. 9, September 1999, pp. 1132-1134.		
	18	DOERR, et al., <i>40-Channel Multi/Demultiplexer with Dynamic Passband Shape Compensation</i> , IEEE Photonics Technology Letters, Vol 13, No. 7, July 2001, pp. 690-692.		
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	20	OKAMOTO, et al., <i>Flat Spectral Response Arrayed-Waveguide Grating Multiplexer with Parabolic Waveguide Horns</i> , Electronics Letters, 29 th August 1996, Vol. 32, No. 18, pp.1661-1662.		
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	22	UETSUKA, et al., <i>Wavelength Division Multi/Demultiplexer with Arrayed Waveguide Grating</i> , IEICE Trans. Electron., Vol E80-C, No. 5, May 1997, pp. 619-624.		

Examiner Signature		Date Considered	03/07/03
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.